Arasan announces the immediate availability of its MIPI I3C Master & Slave IP cores compliant to the just released MIPI I3C Specification Version 1.0.

Jan 10, 2017. San Jose, CA.
MIPI I3C is a chip-to-chip interface that can connect all sensors in a device to the application processor. It consolidates the features of I2C, SPI and UART into a simple two wire interface. The specification achieves clock rates up to 12.5 MHz and provides options for higher performance, high-data rate modes. Another key advantage is that it can be implemented using standard I/O's on a CMOS process. I3C is also a low power interface using a fraction of the power consumed by I2C for the same performance.

Arasan's I3C IP Cores are fully configurable across multiple parameters through simple scripts based on the customer's specifications making it suitable for a variety of Sensor applications.   Arasan's I3C IP has been validated at the RTL and System Level. The Total IP Solution for I3C includes software drivers and HDK's in addition to the IP Cores.

"Arasan I3C has been licensed by early adopters, and semiconductor products incorporating our IP are expected to launch this year", said Sam Beal, Marketing Director at Arasan.

Visit http://arasan.com/products/mipi/i3c for more information

About Arasan
Arasan Chip Systems is a leading provider of Total IP Solutions for mobile and the next generation of Smart applications from home to automobile. Arasan has been a contributing member of MIPI since 2005. Arasan's high-quality, silicon-proven, Total IP Solutions include digital IP cores, analog PHY interfaces, verification IP, hardware verification kits, protocol analyzers, software stacks and drivers, and optional customization services for Ethernet, MIPI, PCIe, USB, UFS, SD, SDIO, eMMC, and UFS. Arasan's Total IP products serve system architects and chip design teams in applications that require silicon-proven, validated IP, delivered with the ability to integrate and verify both digital, analog and software components in the shortest possible time with the lowest risk.

About the MIPI Alliance

MIPI Alliance (MIPI) develops interface specifications for mobile and mobile-influenced industries. Founded in 2003, the organization has more than 275 member companies worldwide, more than 15 active working groups, and has delivered more than 45 specifications within the mobile ecosystem in the last decade. Members of the organization include handset manufacturers, device OEMs, software providers, semiconductor companies, application processor developers, IP providers, test and test equipment companies, as well as camera, tablet and laptop manufacturers. For more information, please visit http://mipi.org

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